



GAU-2631
Dunlop 13-6

#5

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicants(s): Alfred E. Dunlop and
Wilhelm Carl Fischer

Case: 13-6

Serial No.: 09/716,977

Filing Date: November 20, 2000

Examiner: Allyson Purnell

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231

Signature: Alan H. [Signature] Date: May 16, 2001

Title: Gated Clock Recovery Circuit

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TRANSMITTAL OF FORMAL DRAWINGS

Assistant Commissioner for Patents
Washington, D.C. 20231

Attention: Official Draftsperson

SIR:

Applicants submit herewith two (2) sheets of formal drawings in the above-referenced application.

Respectfully submitted,

Kevin M. Mason

Date: May 16, 2001

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